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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*none GCR 9/16/04*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*none GCR 9/16/04*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	<i>GCR 9/16/04</i>			
Verified and Acknowledged Examiner's Signature	Initials			

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## TITLE

Memory bus polarity indicator system and method for reducing the affects of simultaneous switching outputs (SSO) on memory bus timing

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